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PTO/SB/21 (09-04)

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

12

Application Number	10/698,739
Filing Date	October 30, 2003
First Named Inventor	Vo, Binh
Art Unit	2858
Examiner Name	

Attorney Docket Number

015114-068400US

ENCLOSURES (Check all that apply)

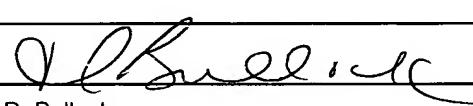
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<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences		
<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)		
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<input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53				
<table border="1"> <tr> <td>Remarks</td> <td>The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.</td> </tr> </table>			Remarks	The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.
Remarks	The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.			

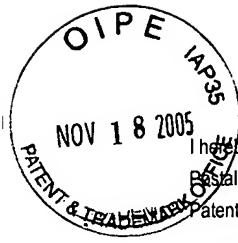
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Townsend and Townsend and Crew LLP		
Signature			
Printed name	Babak S. Sani		
Date	Nov. 15, 2005	Reg. No.	37,495

CERTIFICATE OF TRANSMISSION/MAILING

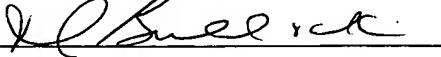
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Signature	
Typed or printed name	D. Bullock
Date	Nov. 15, 2005



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TOWNSEND and TOWNSEND and CREW LLP

By: 

PATENT
Docket 015114-068400
ClientRef A1109

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

VO, Binh et al.

Application No.: 10/698,739

Filed: October 30, 2003

For: FUNCTIONAL FAILURE ANALYSIS
TECHNIQUES FOR PROGRAMMABLE
INTEGRATED CIRCUITS

Examiner: Not Announced

Art Unit: 2133

STATUS REQUEST LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The above-referenced application was filed on October 30, 2003. To date, attorneys for applicants have not received an Office Action from the United States Patent Office.

Please advise of the status of this application.

Respectfully submitted,



Babak S. Sani
Reg. No. 37,495

TOWNSEND and TOWNSEND and CREW LLP
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 15, 2005.

TOWNSEND and TOWNSEND and CREW LLP

By: El Guerick



PATENT
Docket 015114-068400
ClientRef A1109

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

VO, Binh et al.

Application No.: 10/698,739

Filed: October 30, 2003

For: FUNCTIONAL FAILURE
ANALYSIS TECHNIQUES FOR
PROGRAMMABLE INTEGRATED
CIRCUITS

Examiner: Not Announced

Art Unit: 2133

REQUEST FOR CORRECTION OF PATENT OFFICE RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Application Data Sheet submitted with the referenced application on October 30, 2003. As can be seen from the blank priority section, this application does not claim priority to any previously filed applications. However, the initial filing receipt listed both domestic and foreign priority data. The subsequent filing receipt lists domestic priority data.

The priority data appearing on each of the filing receipts is actually priority data for Application No. 10/689,739. This can be seen from the printout of the PAIR system information for that application. A copy of the printout is attached.

As can be seen from the enclosed PAIR system information printout for the present application (10/698,739), no priority, either domestic or foreign, has been claimed. Thus, the initial and subsequent filing receipts are incorrect.

It is respectfully requested that the Patent Office records resulting in the aforementioned incorrect filing receipts be corrected, and that confirmation in the form of a further corrected filing receipt be provided to the undersigned.

Respectfully submitted,



Babak S. Sani
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COPY

Application Data Sheet

Application Information

Application number::

Filing Date::

Application Type:: Regular

Subject Matter:: Utility

Suggested classification::

Suggested Group Art Unit::

CD-ROM or CD-R??::

Number of CD disks::

Number of copies of CDs::

Sequence Submission::

Computer Readable Form (CRF)::

Number of copies of CRF::

Title:: Functional Failure Analysis Techniques for
Programmable Integrated Circuits

Attorney Docket Number:: 015114-068400US

Request for Early Publication:: No

Request for Non-Publication:: Yes

Suggested Drawing Figure::

Total Drawing Sheets:: 3

Small Entity?:: No

Latin name::

Variety denomination name::

Petition included?:: No

Petition Type::

Licensed US Govt. Agency::

Contract or Grant Numbers One::

Secrecy Order in Parent Appl.?:: No

COPY

Applicant Information

Applicant Authority Type:: Inventor
Primary Citizenship Country:: US
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Applicant Authority Type:: Inventor

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Postal or Zip Code of mailing address:: 95132

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Postal or Zip Code of mailing address:: 95134

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Middle Name::
Family Name:: DiCosola
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State or Province of Residence:: CA
Country of Residence:: US

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State or Province of mailing address:: CA
Country of mailing address:: US
Postal or Zip Code of mailing address:: 94566

Applicant Authority Type:: Inventor
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Family Name:: Chughtai
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State or Province of Residence:: CA
Country of Residence:: US
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Country of mailing address:: US
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Applicant Authority Type:: Inventor
Primary Citizenship Country:: Malaysia
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State or Province of Residence::

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State or Province of mailing address::
Country of mailing address:: Malaysia
Postal or Zip Code of mailing address::

Correspondence Information

Correspondence Customer Number:: 26059

Representative Information

Representative Customer Number:: 26059

Domestic Priority Information

Application:: Continuity Type:: Parent Application:: Parent Filing Date::

Foreign Priority Information

Country:: Application number:: Filing Date::

Assignee Information

Assignee Name:: Altera Corporation
Street of mailing address:: 101 Innovation Drive
City of mailing address:: San Jose
State or Province of mailing address:: CA
Country of mailing address:: US
Postal or Zip Code of mailing address:: 95134



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PATENT APPLICATION INFORMATION RETRIEVAL



Search results for application number: 10/689,739

Application Number:	10/689,739	Customer Number:	-
Filing or 371(c) Date:	10-22-2003	Status:	Patented Case
Application Type:	Utility	Status Date:	08-17-2005
Examiner Name:	RAMIREZ, RAMON O	Location:	ELECTRONIC
Group Art Unit:	3632	Location Date:	-
Confirmation Number:	4342	Earliest Publication No:	US 2004-0135057 A1
Attorney Docket Number:	66905-015-7	Earliest Publication Date:	07-15-2004
Class/ Sub-Class:	248167	Patent Number:	6,938,872
First Named Inventor:	Knud Nygaard, Vodskov, (DK)	Issue Date of Patent:	09-06-2005
Title Of Invention:	MACHINE SHOE FOR THE SUPPORT OF MACHINES AND A METHOD		

Select Search Option

Image File Wrapper

Search

File History

Foreign Priority

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Maintenance Fees - View payment windows

Maintenance Statement - View 04 year payment window

Maintenance Fees Available: Mon-Fri 5:30 AM to Midnight, Sat-Sun-Hol. 7:30 AM to 8:00 PM E.T.

Parent Continuity Data

Description	Parent Number	Parent Filing or 371(c) Date	Parent Status
This application claims benefit of Provisional Application	60/440,330	01-16-2003	Expired
Child Continuity Data			



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10/698,739 Functional failure analysis techniques for programmable integrated circuits

Application Transaction Data	Image File History	Published Documents	Publication Dates	Address & Attorney/Agent
------------------------------	--------------------	---------------------	-------------------	--------------------------

Application Number: 10/698,739 **Customer Number:** -
Filing or 371 (c) Date: 10-30-2003 **Status:** Docketed New Case - Ready for Examination

Application Type: Utility **Status Date:** 10-28-2005
Examiner Name: DECADY, ALBERT **Location:** ELECTRONIC

Group Art Unit: 2133 **Location Date:** -
Confirmation Number: 3284 **Earliest Publication No:** US 2005-0022085 A1

Attorney Docket Number: 015114-068400US **Earliest Publication Date:** 01-27-2005
Class / Subclass: 714/738 **Patent Number:** -

First Named Inventor: Binh Vo, San Jose, CA **Issue Date of Patent:** -
Title of Invention: Functional failure analysis techniques for programmable integrated circuits

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- Send general questions about USPTO programs to the [USPTO Contact Center \(UCC\)](#).
- If you experience technical difficulties or problems with this application, please report them via e-mail to [Elec](#)